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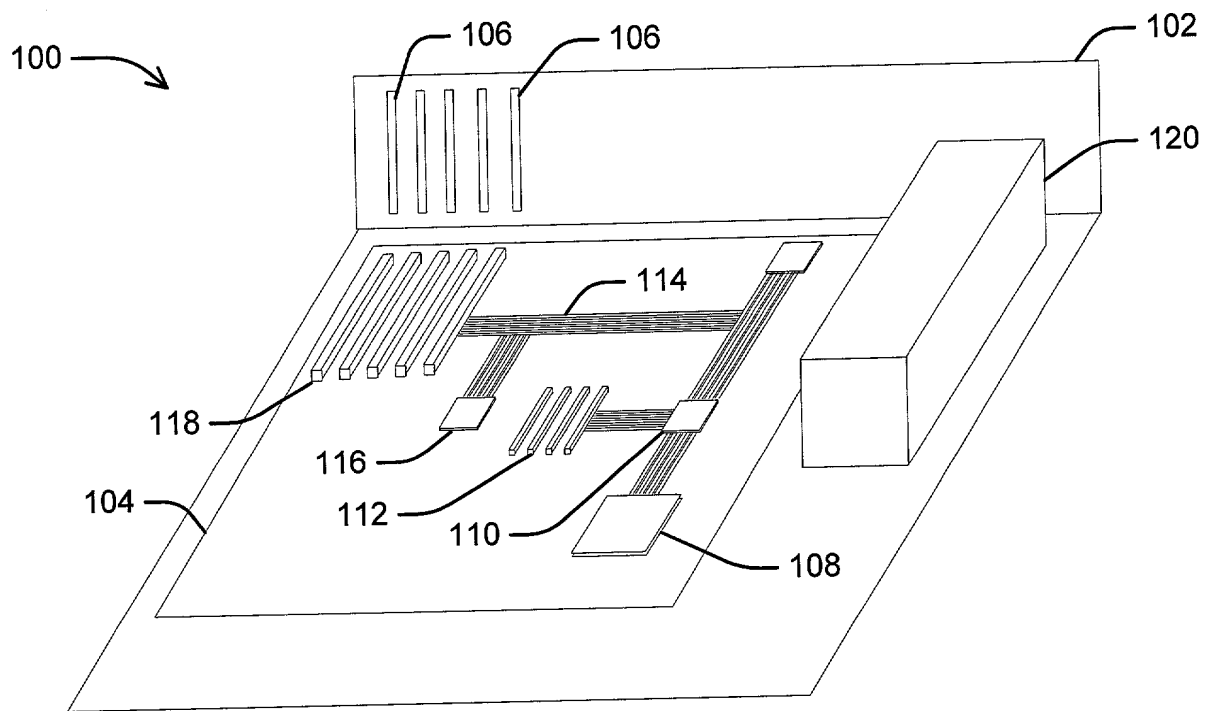


FIG 1

FIG. 2

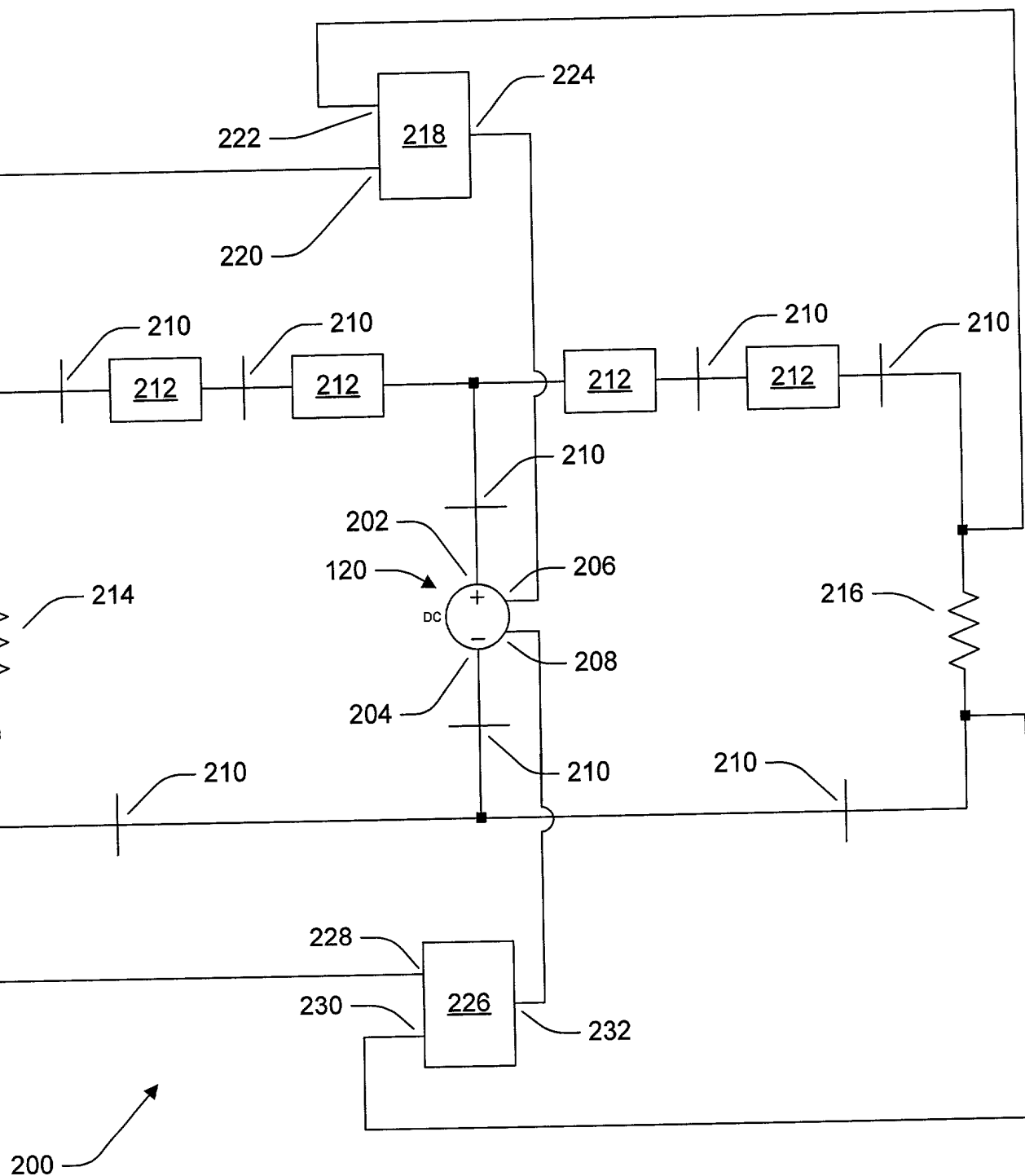


FIG 2

The diagram shows a circuit block 218 enclosed in a dashed rectangle. It contains a differential pair of transistors 302, with inputs 220 and 222. The gates of these transistors are connected to a common-mode input 300. The drains of the transistors are connected to a current mirror load consisting of a resistor 308 and a transistor 306. The gates of the current mirror transistor 306 and the differential pair transistors 302 are connected to a biasing network consisting of a resistor 312 and a transistor 314, which is connected to ground. The output of the current mirror is connected to a node 224.

FIG 3

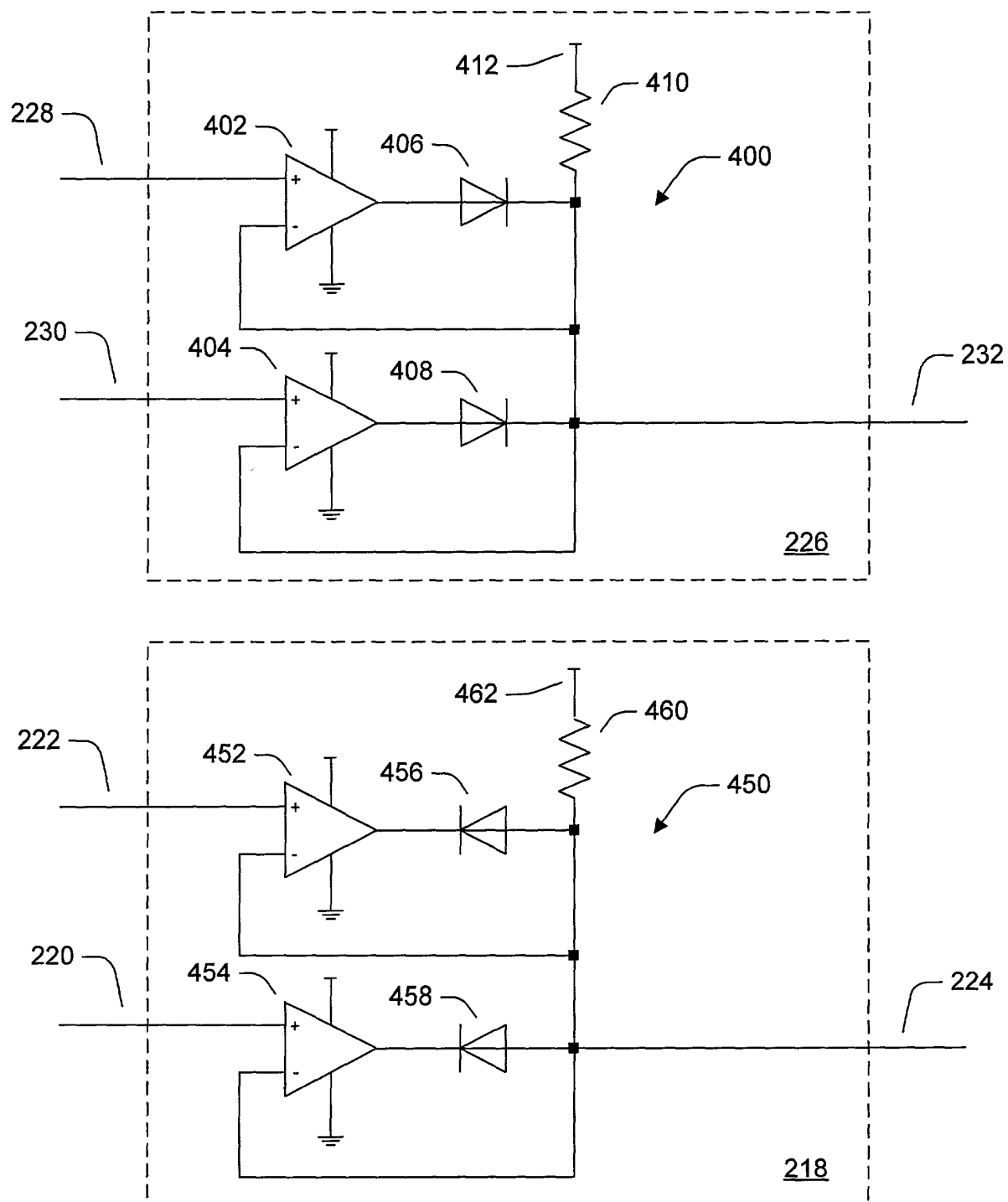


FIG 4

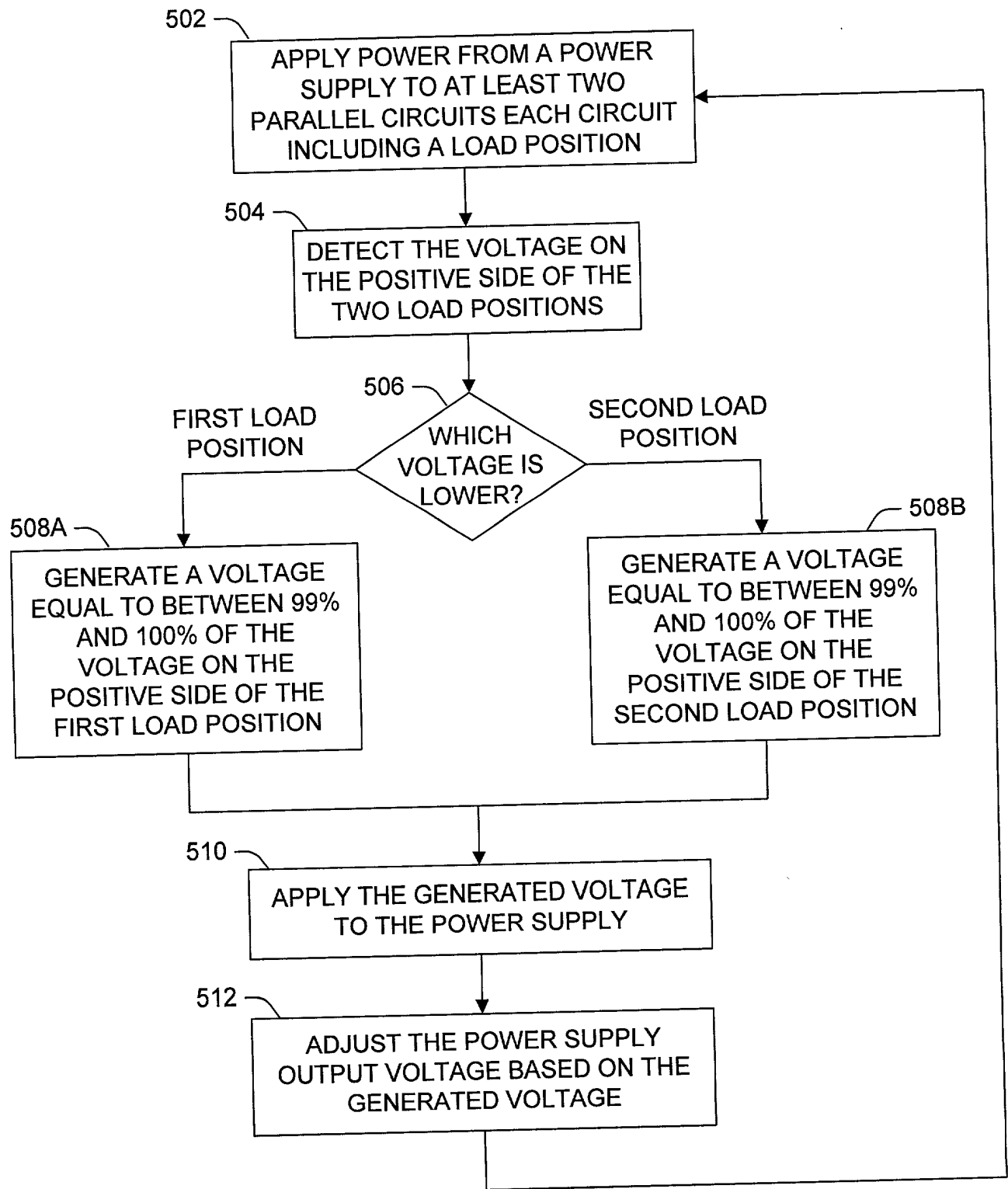


FIG 5

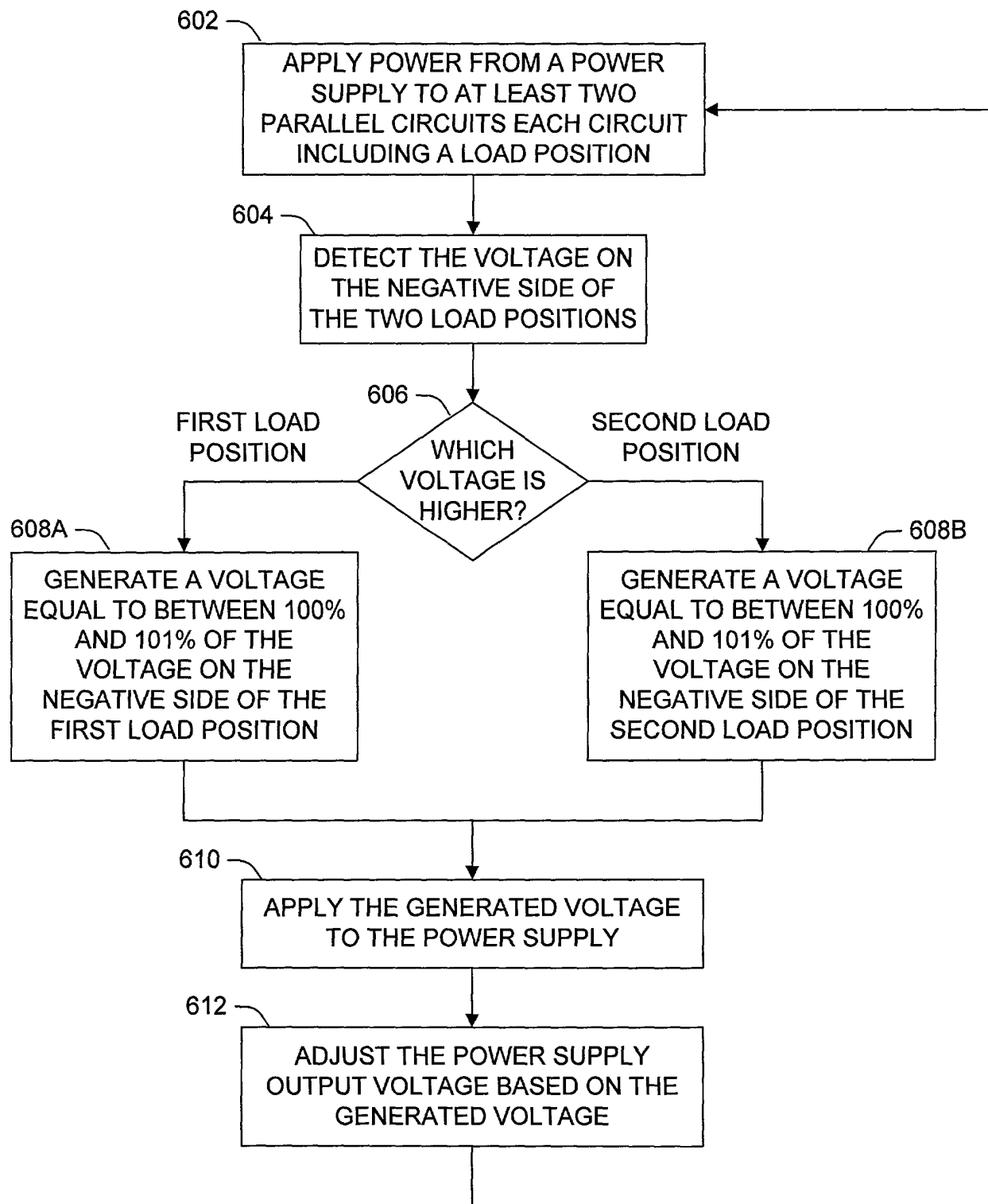


FIG 6